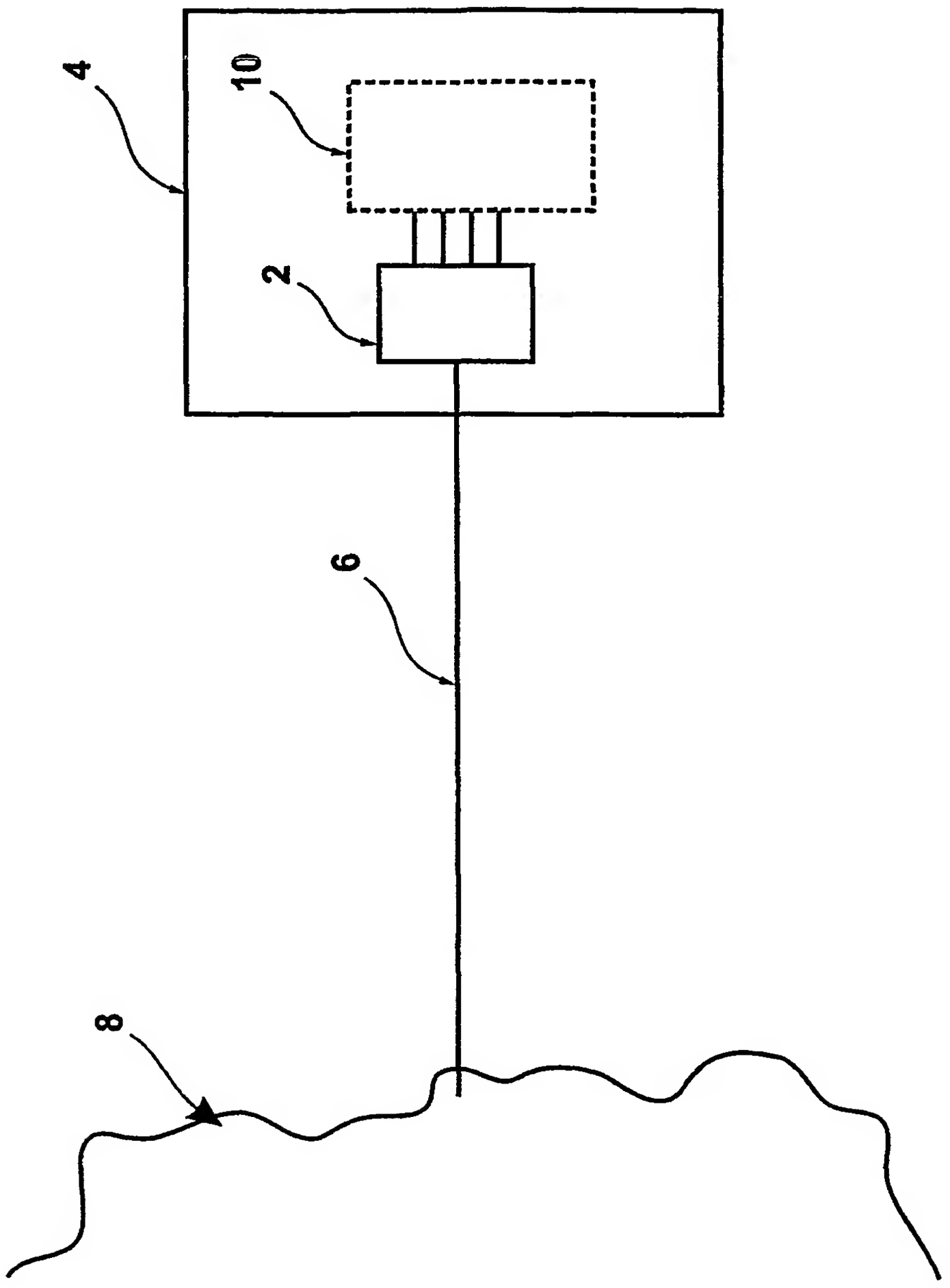
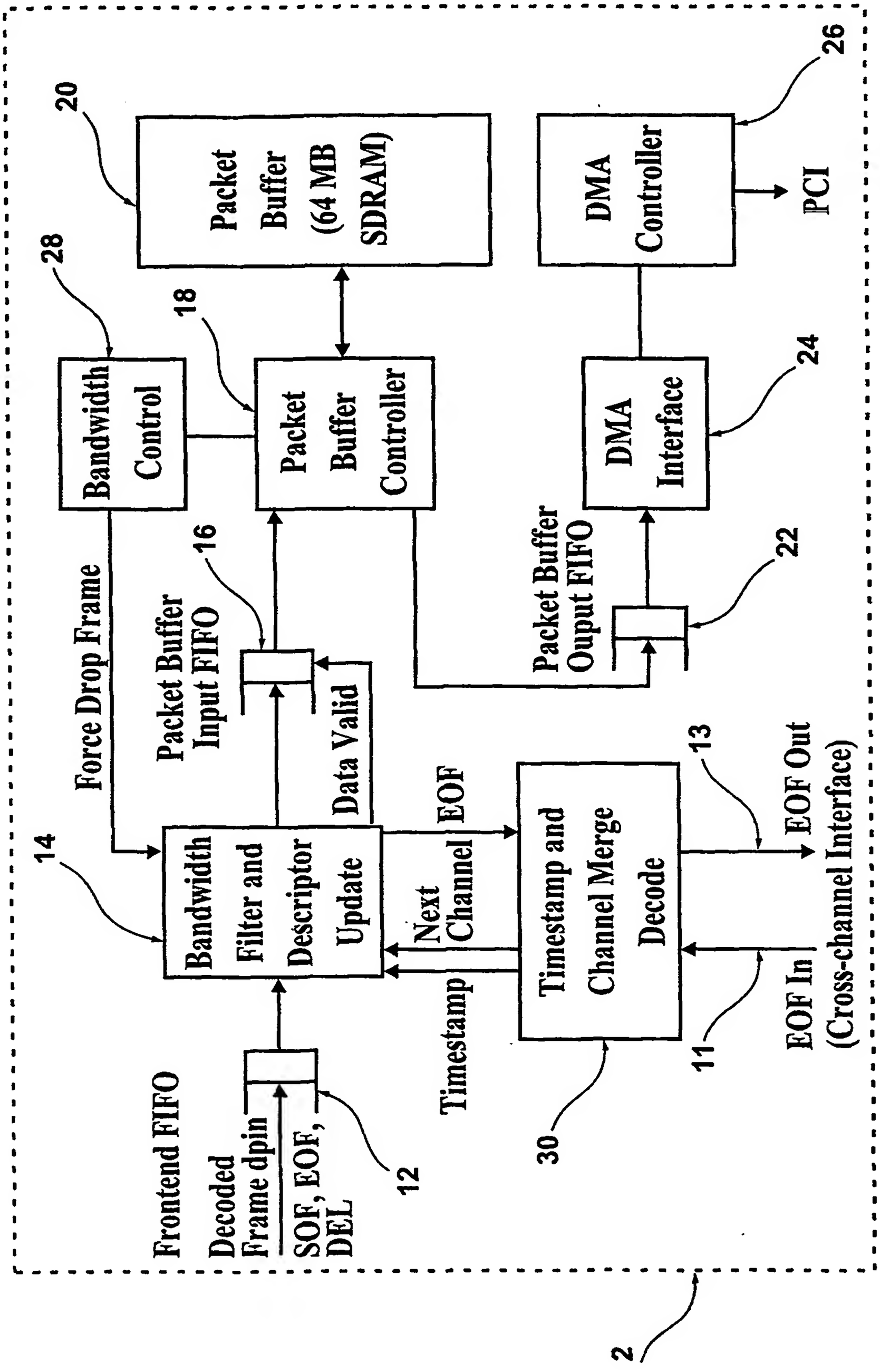
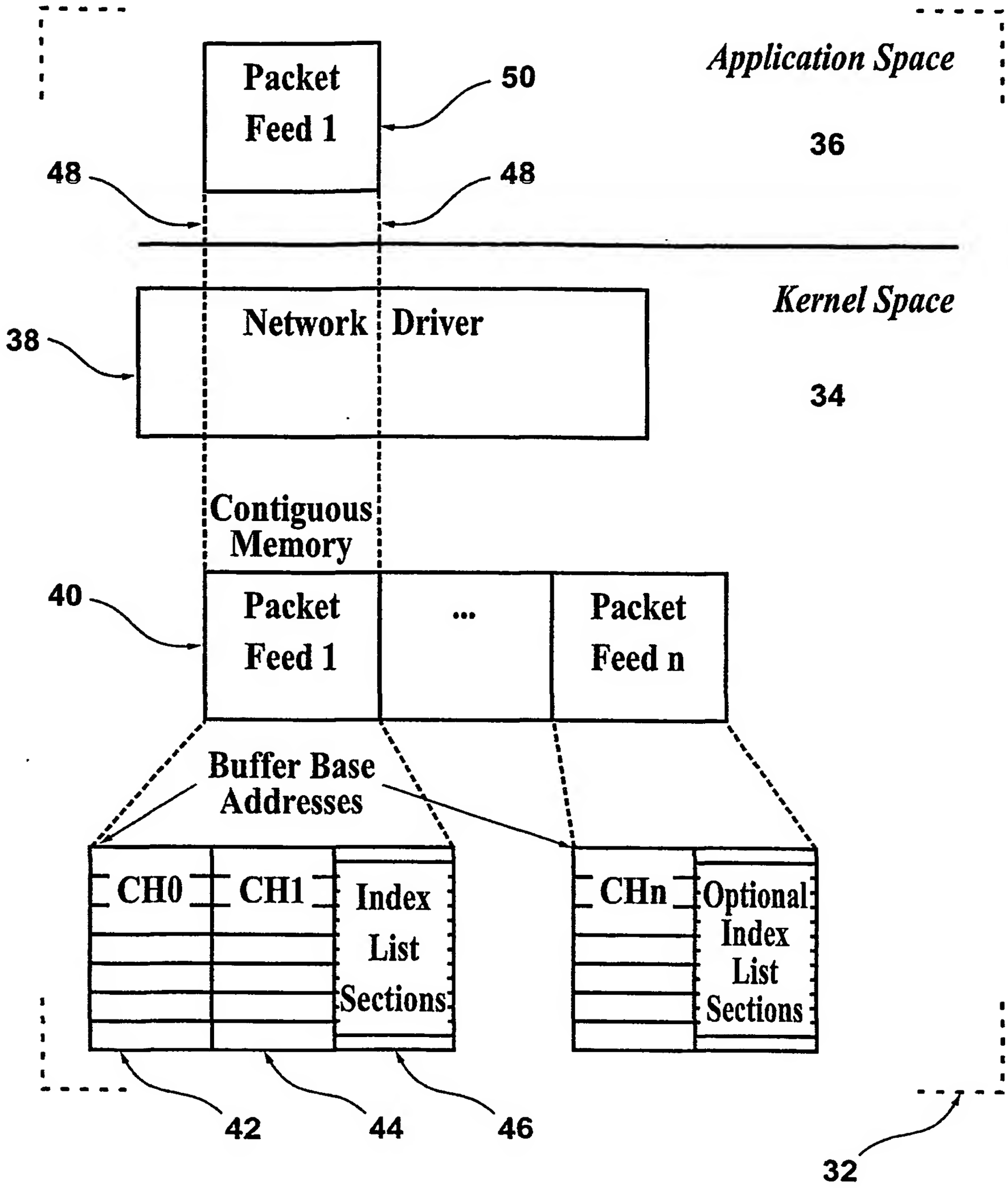


SUBSTITUTE SHEET (RULE 26)


20
Packet Buffer (64 MB SDRAM)
26
DMA Controller
PCI
28
18
Bandwidth Control
Packet Buffer Controller
DMA Interface
24
Force Drop Frame
Packet Buffer Input FIFO
16
Packet Buffer Ouput FIFO
22
Data Valid
EOF
14
Bandwidth Filter and Descriptor Update
Next Channel
Timestamp and Channel Merge Decode
13
EOF Out
EOF In
Cross-channel Interface)
Frontend FIFO
Decoded Frame dpin
SOF, EOF, DEL
12
Timestam
11
30
2


Application Space
36
Packet Feed 1
50
48
48
Kernel Space
34
Network Driver
38
Contiguous Memory
40
Packet Feed 1
...
Packet Feed n
Buffer Base Addresses
CH0
CH1
Index List Sections
CHn
Optional Index List Sections
42
44
46
32